PTO/SB/08A (10-01)

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Substitute for form 1449A/PT()	Complete if Known		
		Application Number	filed herewith 10/79/do 29	
INFORMATION DIS	CLOSURE	Filing Date	filed herewith	
STATEMENT BY A	PPLICANT	First Named Inventor	Donald A. Hitko	
		Group Art Unit		
(use as many sheet as necessary)		Examiner Name		
Sheet 1 o	f 2	Attorney Docket Number	03-1119	

			U.S. PATENT	DOCUMENTS	
Examiner Initials*	Cite No.	Document Number Number – Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Passages or Relevant Figures Appear
ブ.し	1	US- 5,394,122	February 28, 1995	Conway, et al.	
T-L	2	US- 5,134,400	July 28, 1992	Hash	
JEL	3	US- 5,128,674	July 7, 1992	Kong, et al.	
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Examiner Signature	Caulus	Date Considered	12/23/	2004.

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¹ Unique citation designation number. ² See attached Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the senal number of the patent documents. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard St.16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

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STA	TEMENT B	Y AP	PLICANT	First Named Inventor	Donald A. Hitko	
				Group Art Unit		
(use as many sheet as necessary)		Examiner Name				
Sheet	2	of	2	Attorney Docket Number	03-1119	

		OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS	
r	Cite	Include name of the author (in CAPITAL LETTERS), title of the article (when Appropriate), title of the	T 2
Examiner Initials*	No. 1	item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	l '
IIIIIais	NO.	<u> </u>	
171	4	CECIL, J.B. AND HOWARD, J.G.; A Fully Monolithic 6-Bit, Multiplying D/A Converter; IEEE	1
<u> </u>		International Solid-State Circuits Conference Proceedings; Feb. 1971, p52-53.	
T /	5	KRABBE, H. AND MOLINARI, F; A Monolithic Multiplying D/A Converter Circuit; IEEE	
1 J · L	°	International Solid-State Circuits Conference Proceedings; Feb. 1971, p54-55, 198.	
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